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| PTO-1449 (Modified) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT | Attorney Docket No.: 2003-IP-09595 / HESI.105897 | Serial Number: 10/628,781 |
| | Applicant: Chris Chuter | |
| | Filing Date: July 28, 2003 | Group: |

U.S. PATENT DOCUMENTS

| EXAMINER INITIAL | PATENT NUMBER | ISSUE DATE | INVENTOR | CLASS | SUBCLASS | FILING DATE |
|---------------------|--|---------------|--------------------|-------|----------|-------------|
| AL | Pub. No. 2002/0172401 (09/936,780) | 11/2002 | Lees, Jack, et al. | 382 | 109 | 9/17/01 |
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FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION

| EXAMINER INITIAL | DOCUMENT NUMBER | PUBLICATION DATE | COUNTRY OR PATENT OFFICE | CLASS | SUBCLASS | TRANSLATION | |
|---------------------|--------------------|---------------------|-----------------------------|-------|----------|-------------|----|
| | | | | | | YES | NO |
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| EXAMINER INITIAL | OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place Of Publication) |
|--|--|
| AL | Mark J. Kilgard, "A Practical and Robust Bump-mapping Technique for Today's GPUs," July 5, 2000, pp. 1-39, NVIDIA Corporation, Santa Clara, CA |
| AL | Jack Lees, "Constructing Faults from Seed Picks by Voxel Tracking," March 1999, pp. 338, 340, <i>The Leading Edge</i> . |
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| EXAMINER | DATE CONSIDERED |
| AL | 2/13/06 |
| EXAMINER: Initial citation if reference was considered. Draw line through citation if not in conformance to MPEP 609 and not considered. Include copy of this form with next communication to applicant. | |